

FIG. 1

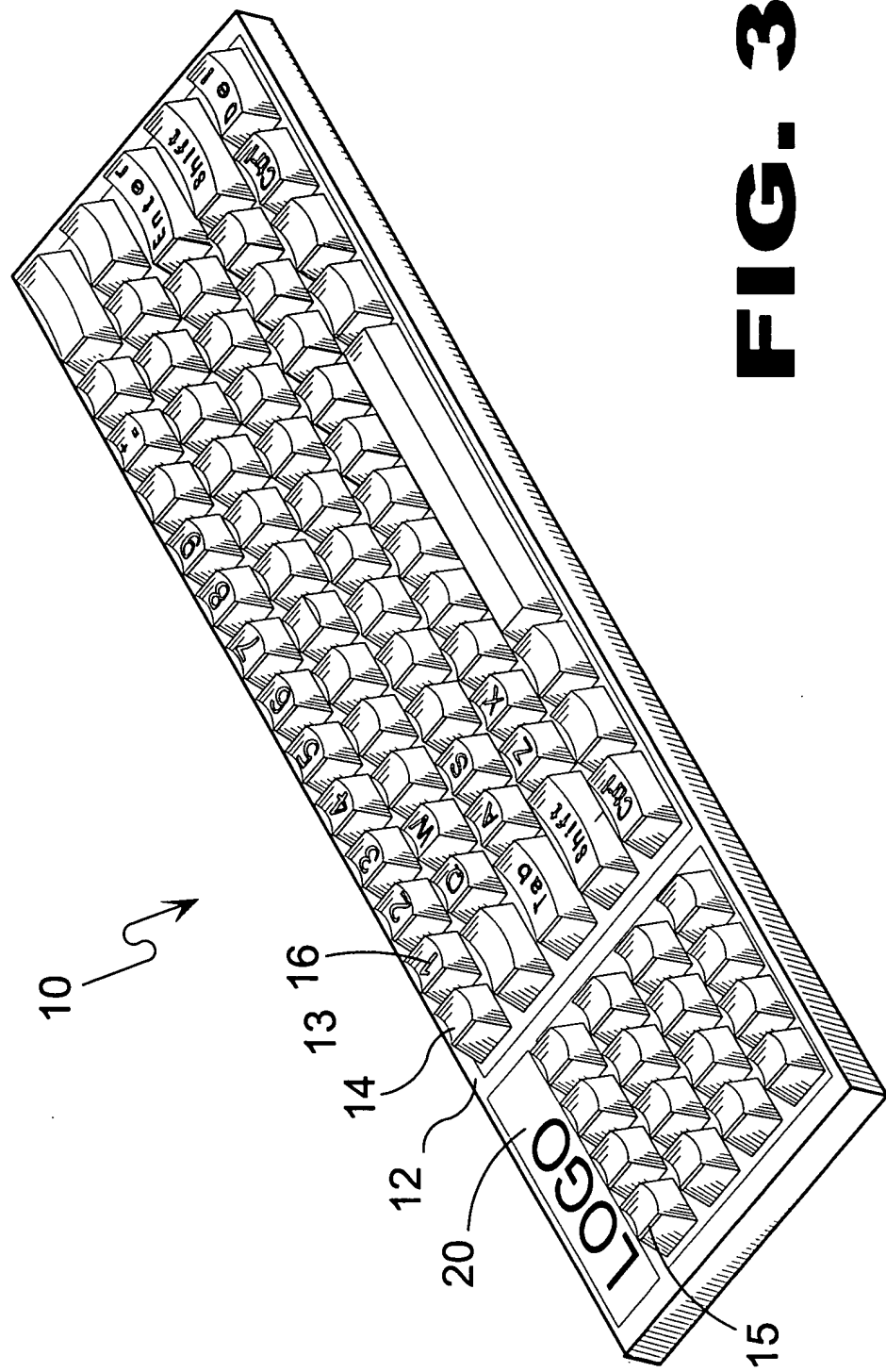


FIG. 3

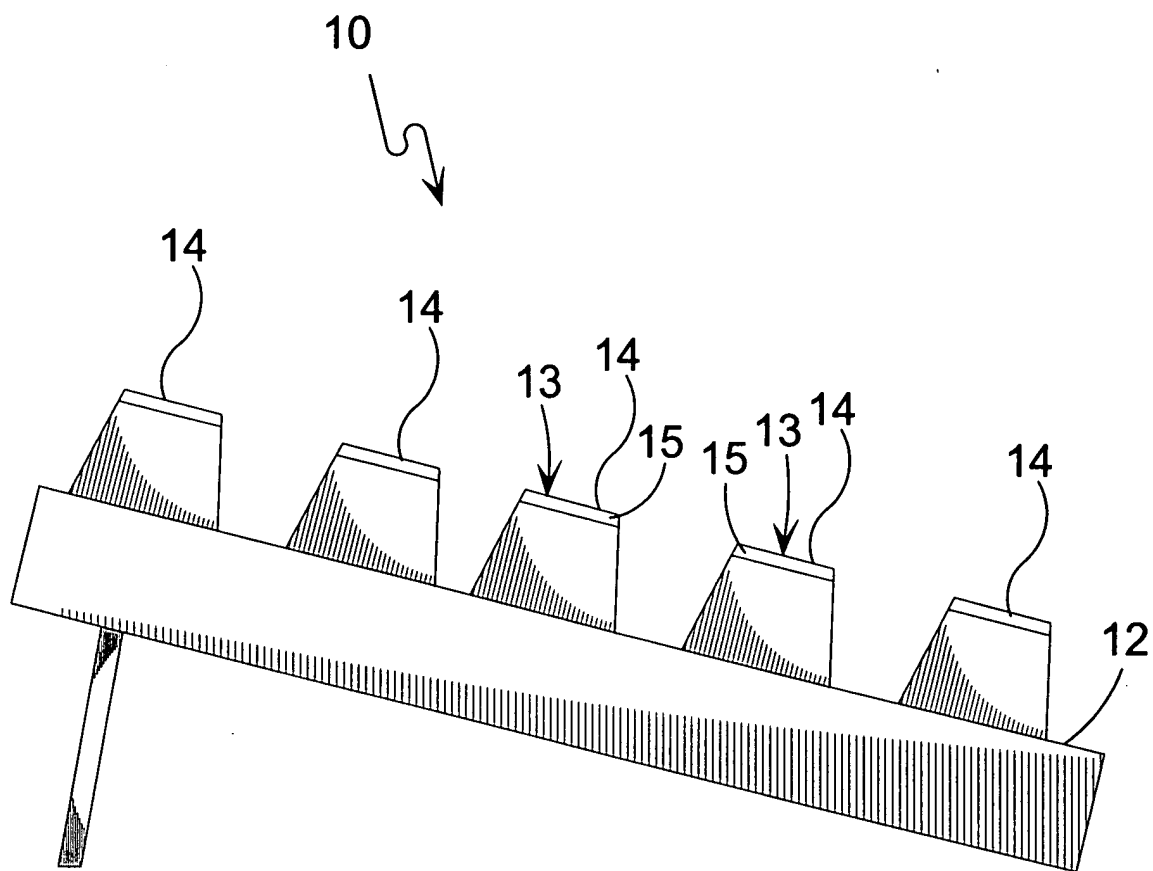
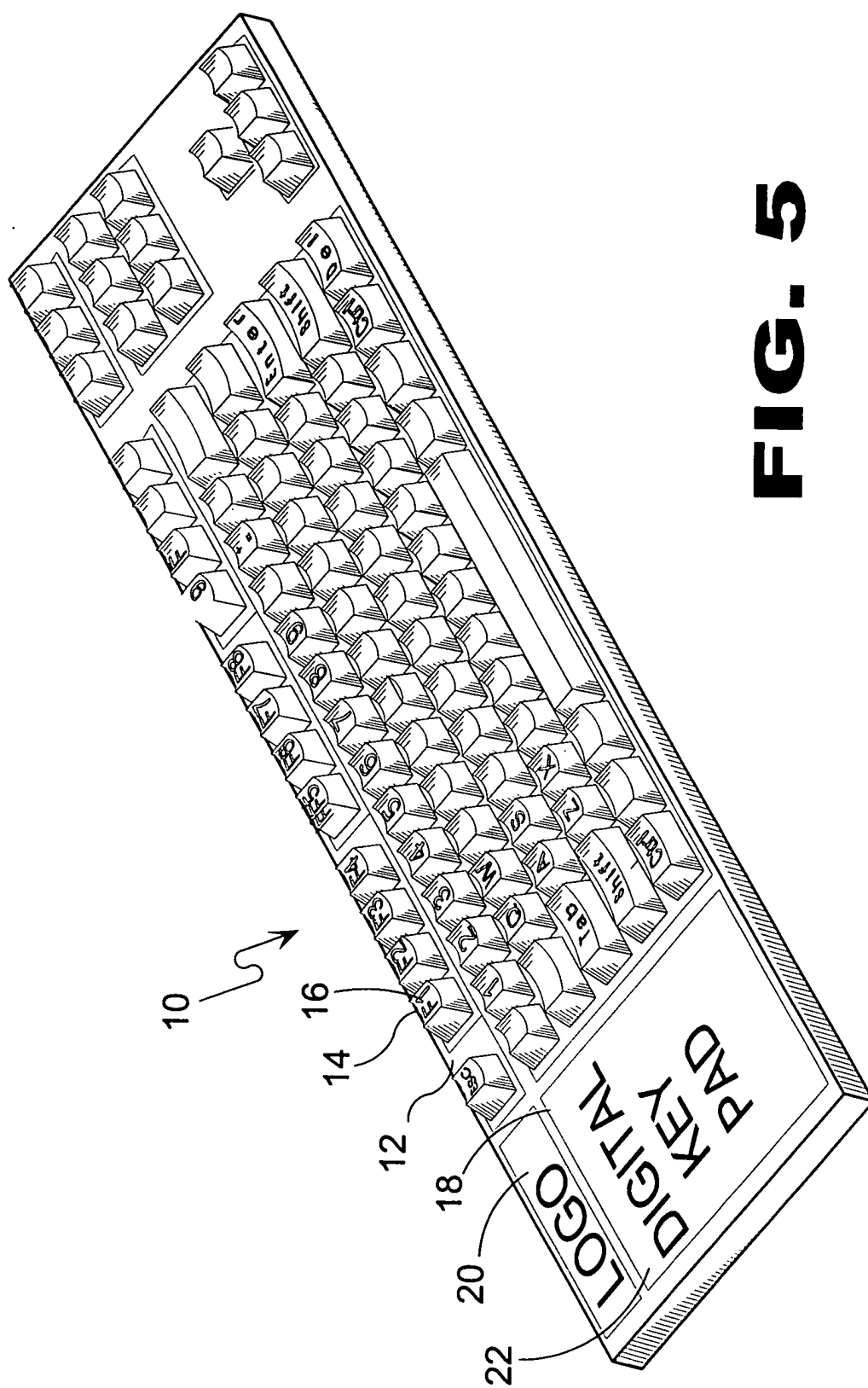
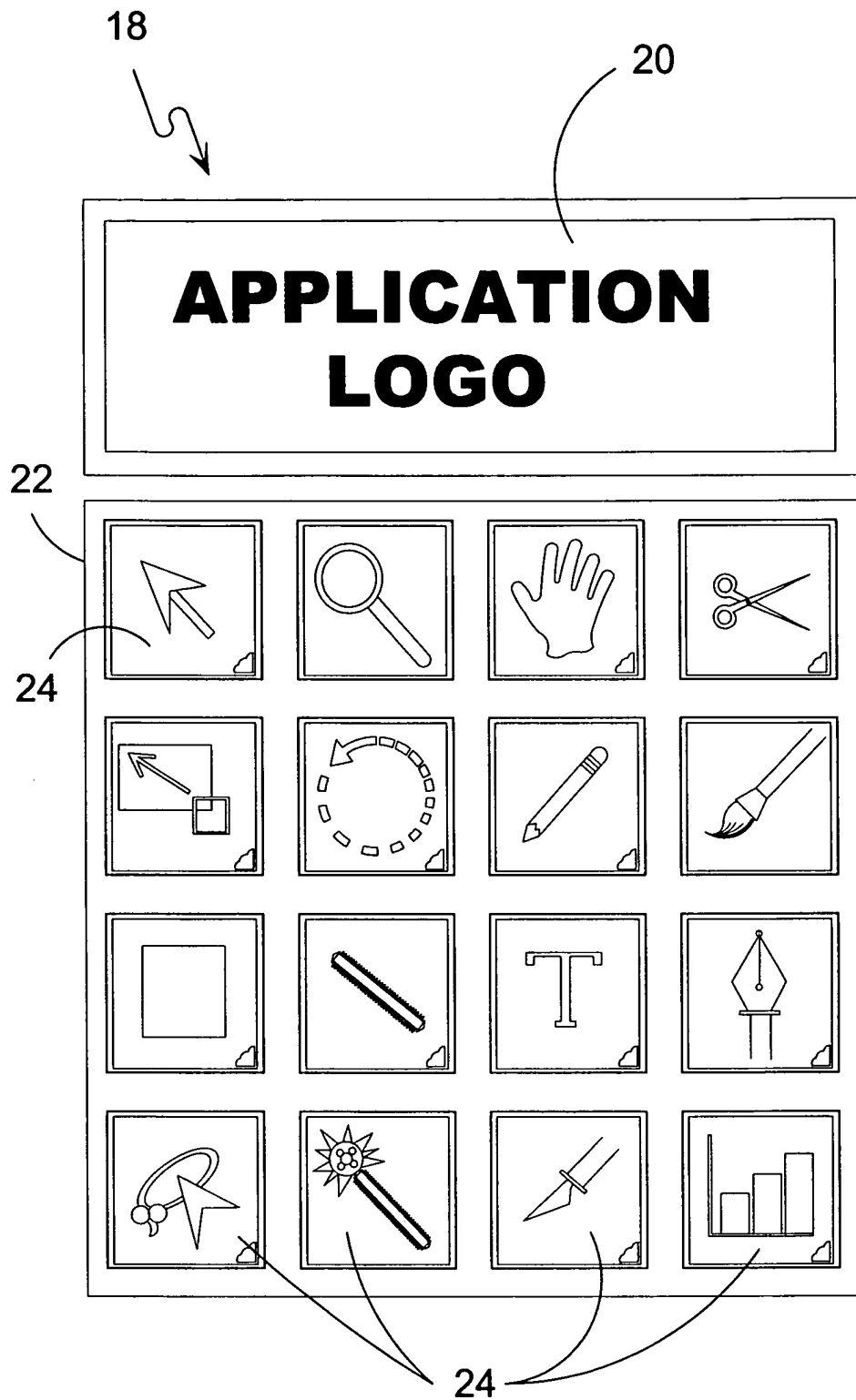


FIG. 4



LOGIC



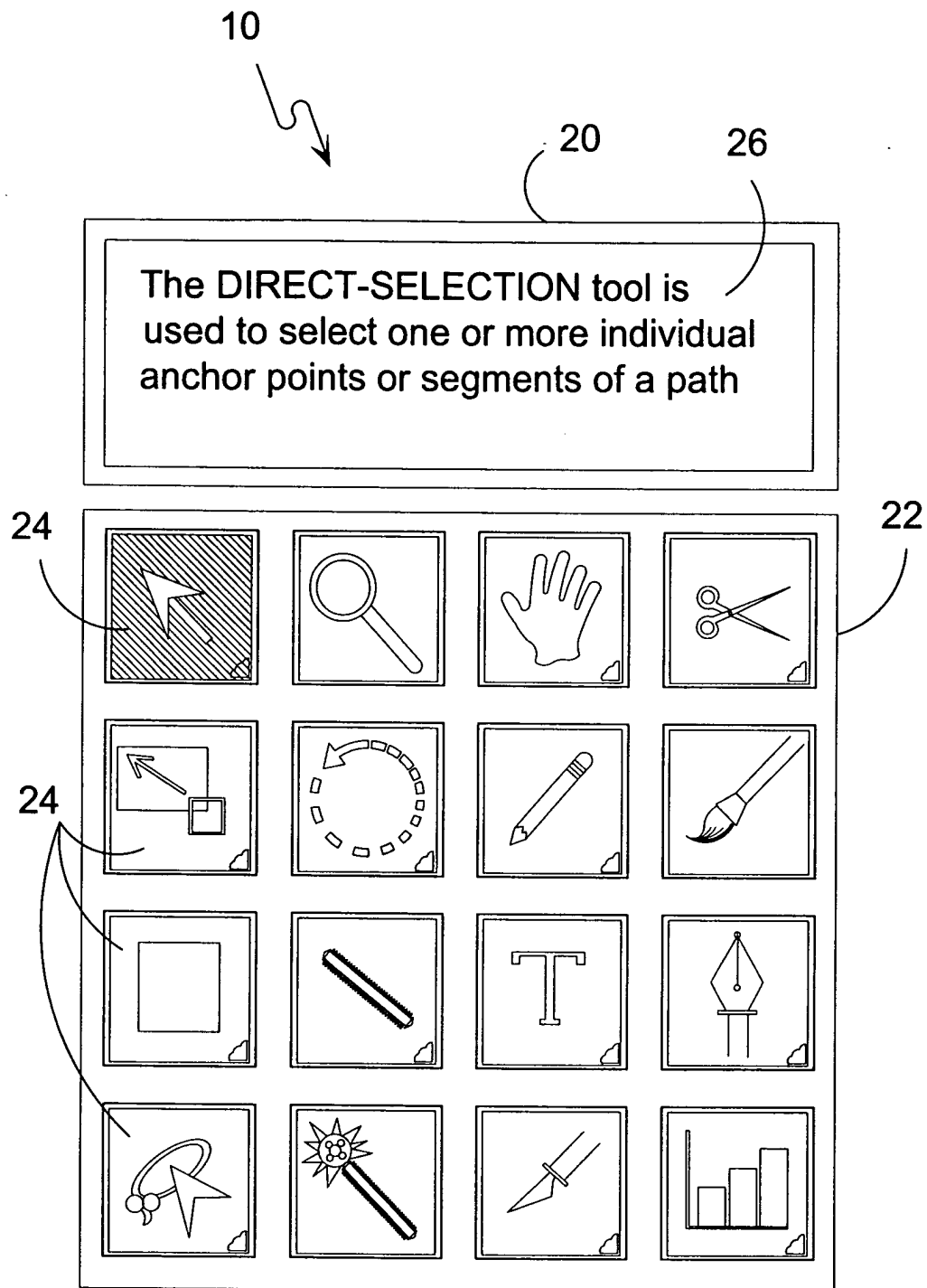


FIG. 7

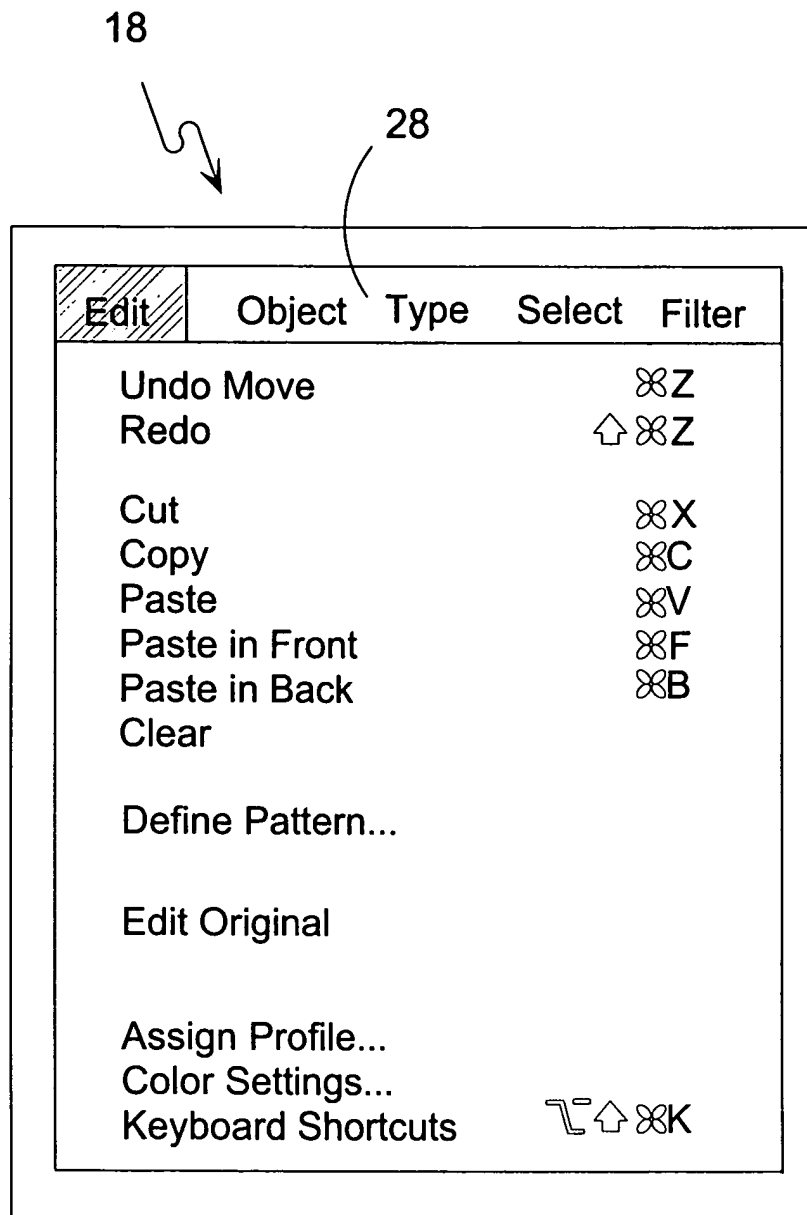


FIG. 8

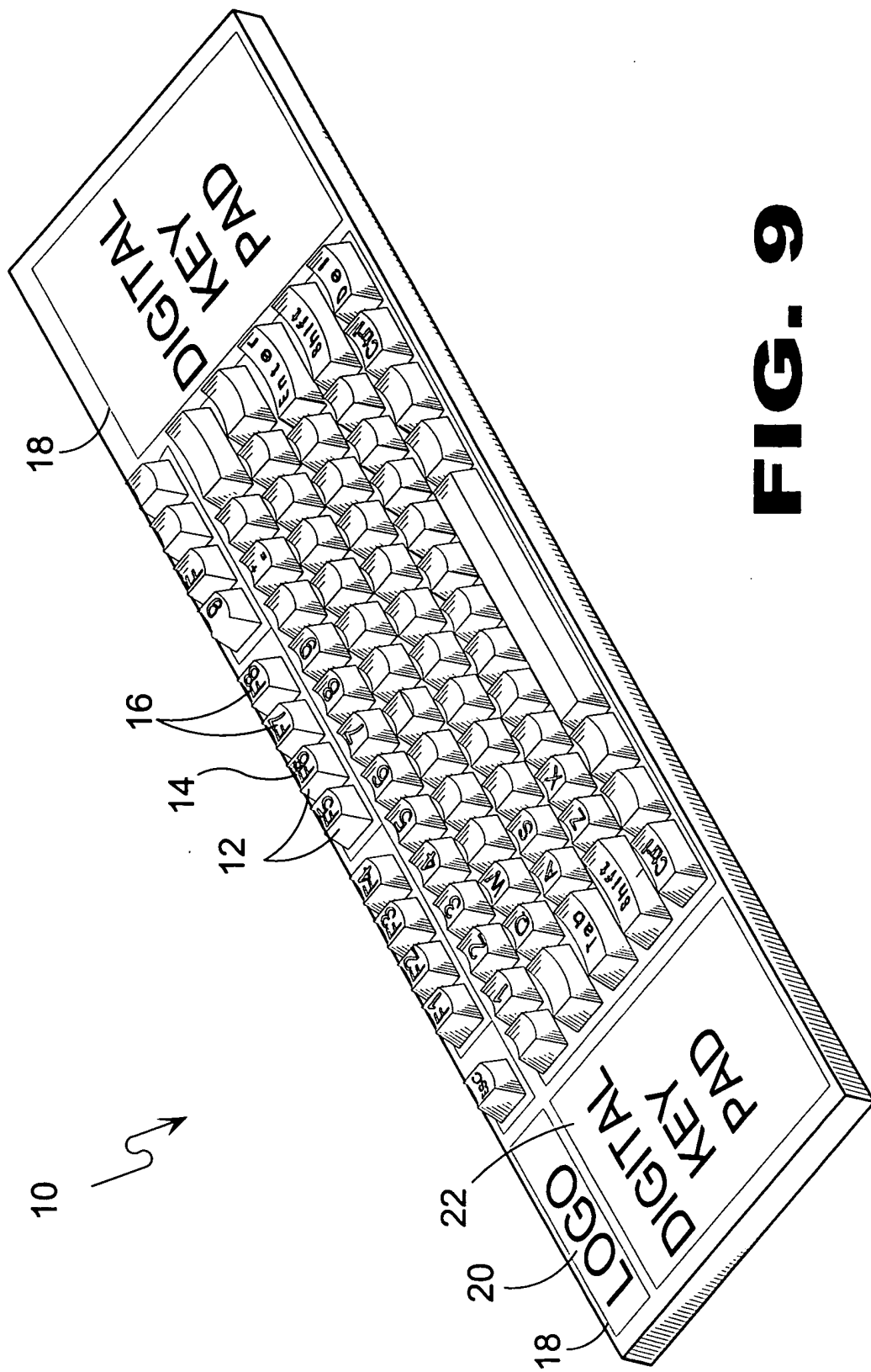


FIG. 9

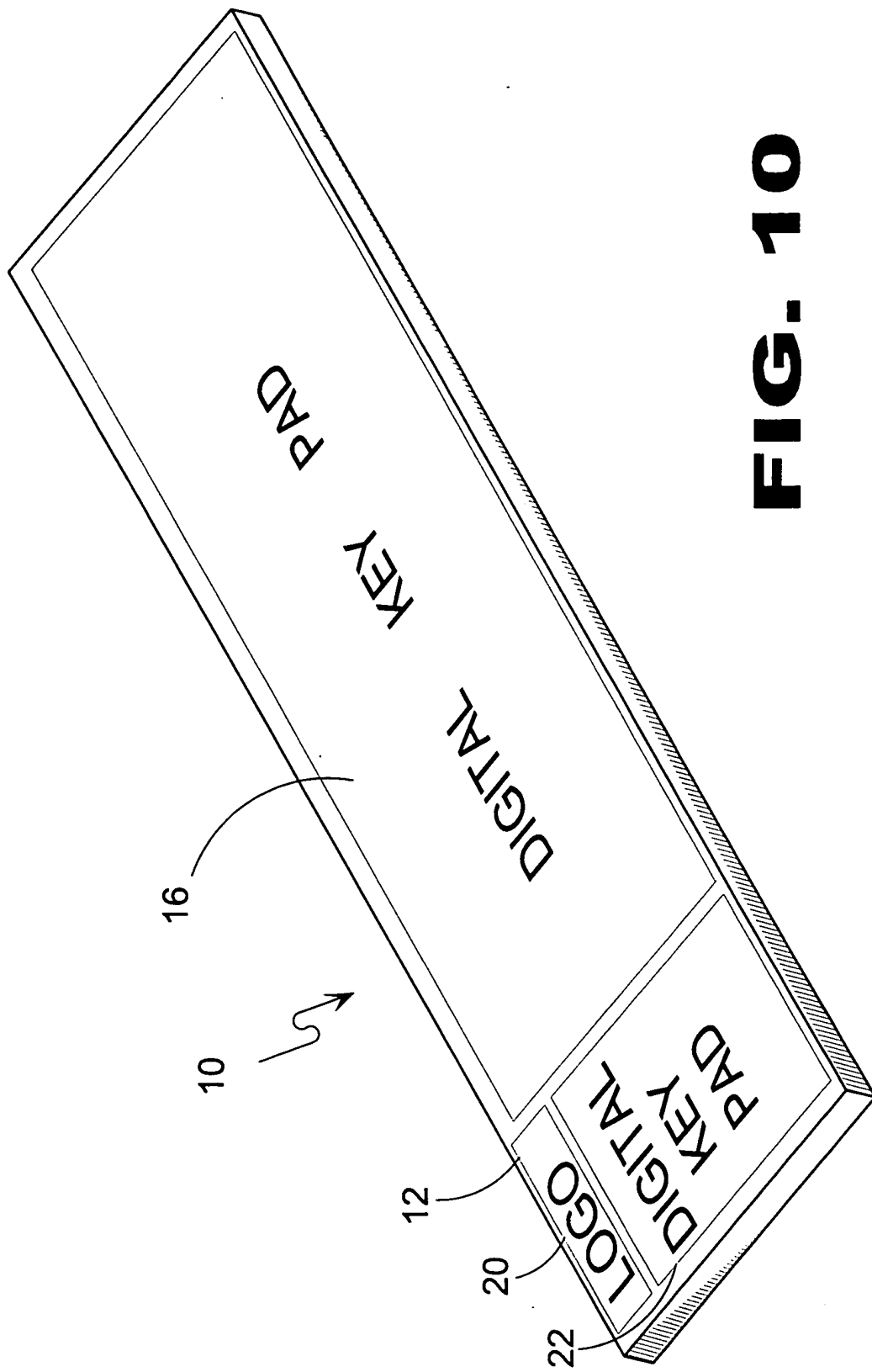


FIG. 10

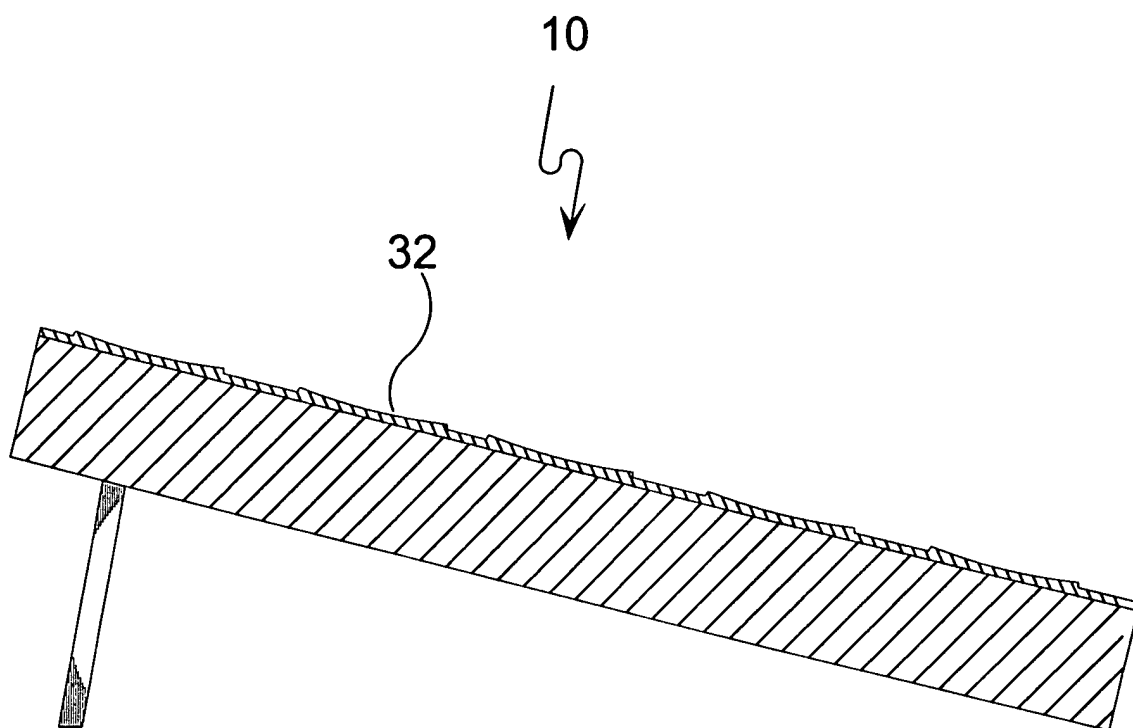


FIG. 11

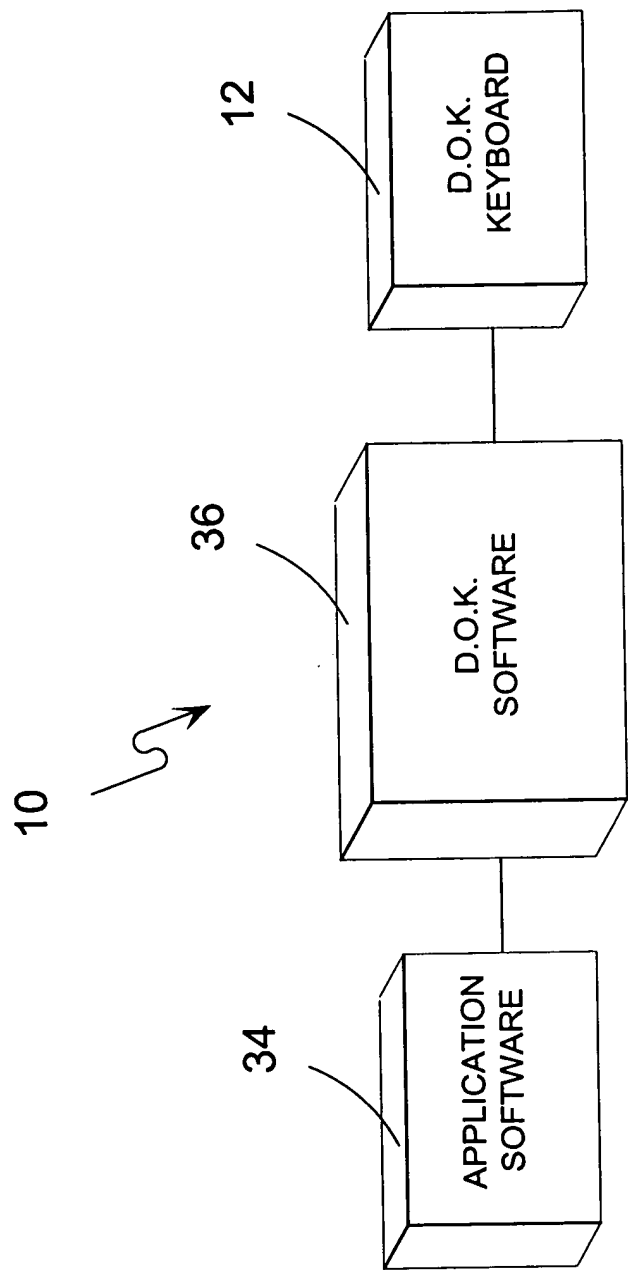


FIG. 12

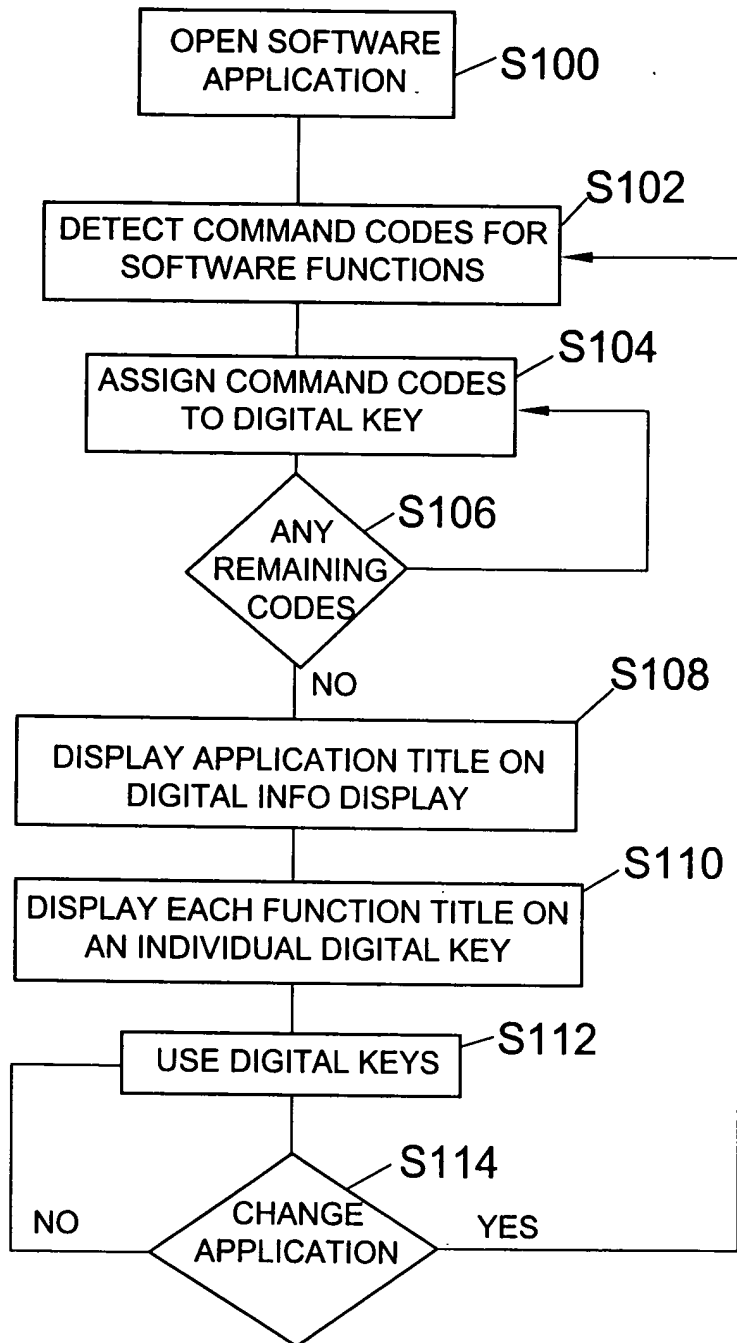


FIG. 13

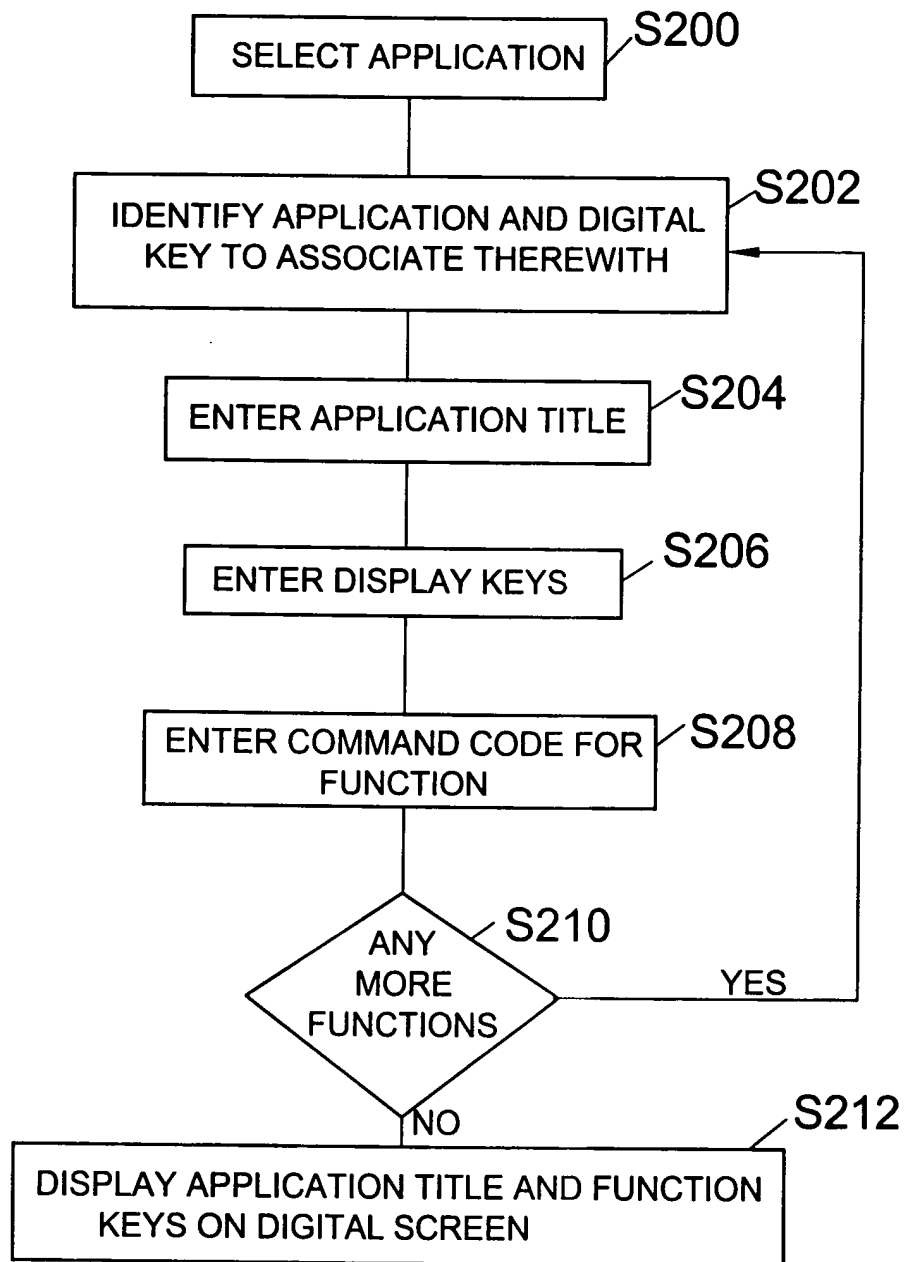


FIG. 14